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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10082554	FILING DATE 02/25/2002	CLASS 267 438	SUBCLASS 763	GAU 26 28	EXAMINER Horne
**APPLICANTS: Bauch Lothar; Zell Thomas; Lehr Matthias; Kieslich Albrecht;					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A CON OF PCT/DE00/02854 08/23/2000					
** FOREIGN APPLICATIONS VERIFIED: GERMANY 199 39 852.6 08/23/1999					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials				ATTORNEY DOCKET NO 1999P2628	
TITLE : Stacked via with specially designed landing pad for integrated semiconductor structures					

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436 (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	Total Claims
			Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Primary Examiner	Sheets Drwg. Figs. Drwg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Applicant Examiner
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